

In the Claims:

Kindly amend the claims as follows:

1. (Previously presented) A repeater system for receiving a modulated input signal and for transmitting a modulated output signal, comprising:

a clock oscillator for providing a repeater system clock;

a demodulator for demodulating an input signal to obtain a demodulated signal, the input signal being the modulated input signal or a signal derived from the modulated input signal, the demodulator including:

a first mixer which is arranged for receiving the input signal at an input port, the first mixer additionally having a local oscillator port and an output port;

a first controllable oscillator connected to the local oscillator port of the first mixer, the first controllable oscillator being arranged for providing an output signal which is derived from the repeater system clock;

feedback means having a first input for receiving a frequency of the signal at the output port of the first mixer as an actual value and a second input for receiving a desired value for the frequency of the signal at the output port of the first mixer, the feedback means being arranged for determining a first control value, which is applied to the controllable first oscillator, the control value being determined such that the actual value approaches the desired value;

a modulator for modulating the demodulated signal to obtain a modulated signal, the modulated signal being the modulated output signal or a signal from which the modulated output signal is derived, including:

a second mixer which is arranged for receiving the demodulated signal at an input port and for providing the modulated output signal at an output port;

a second controllable oscillator connected to the local oscillator port of the second

mixer, the second controllable oscillator being arranged for providing an output signal which is derived from the repeater system clock, the second oscillator being controlled by a second control value; and

a controller for receiving the first control value and for providing the second control value, the controller being arranged for determining the second control value on the basis of the first control value and a predetermined value of the frequency of the modulated output signal such that the frequency of the modulated output signal approaches the predetermined value.

2. (Previously presented) The repeater system of claim 1, which is arranged for receiving the modulated input signal, which frequency is higher than a predetermined demodulator input frequency, the repeater system further comprising:

a tuner for down-converting the modulated input signal to obtain a modulated intermediate frequency signal forming the demodulator input signal, the tuner being arranged for using a tuner oscillator signal, which is derived from the repeater system clock; and

wherein the controller is arranged for determining the second control value further on the basis of the ration of the modulated input signal frequency and the repeater system clock frequency.

3. (Previously presented) The repeater system of claim 1, which is arranged for transmitting the modulated output signal, which frequency is higher than a predetermined modulator output frequency, the repeater system further comprising:

an up-converter for up-converting the modulated signal to obtain the modulated output signal, the up-converter using a converter oscillator signal which is derived from the repeater

system clock; and

wherein the controller is arranged for determining the second control value further on the basis of the ratio of the frequency of the modulated output signal and the repeater system clock frequency.

4. (Previously presented) The repeater system of claim 1, further comprising an analog/digital converter having its output connected to the demodulator for providing digital demodulator input signal.

5. (Previously presented) The repeater system of claim 1, wherein the modulated signal has been modulated by a complex I/Q modulation technique.

6. (Previously presented) The repeater system of claim 4, wherein the first and second controllable oscillators are numerically controlled oscillators, each numerically controlled oscillator having:

a N-bit phase accumulator; and

a sine/cosine lookup table for outputting a sine signal and a cosine signal, the frequencies thereof being determined by the first and second control values, respectively, each control value being determined by the ratio of a respective increment and a respective table length of the respective lookup tables.

7. (Currently amended) The repeater system of claim 6, wherein the first mixer is a complex multiplier receiving at its local oscillator port the sine signal and the cosine signal output by the first numerically controlled oscillator, and outputting at its output port a complex signal having an in phase component and ~~an~~ a quadrature component.

8. (Previously presented) The repeater system of claim 6, further comprising a decision stage arranged for receiving at an input port thereof the I/Q components and outputting at an output thereof phase values on the basis of the I and Q components, these phase values representing the demodulated signal.

9. (Previously presented) The repeater system of claim 8, wherein the feedback means comprises:

a phase discriminator arranged for receiving, as actual values, the I and Q components and, as desired values, the phase values output by the decision stage, and for outputting a phase difference between the actual values and the desire values; and

a loop filter for receiving the phase difference and for outputting the increment for the first numerically controlled oscillator such that the output frequency of the numerically controlled oscillator equals the frequency of the modulator input signal.

10. (Previously presented) The repeater system of claim 1, wherein the feedback means is arranged to determine the first control value such that the desired value is zero.

11. (Previously presented) The repeater system of claim 1, wherein the demodulator has a specified capture and tracking range and wherein the required accuracy of the clock oscillator is determined by the specified capture and tracking range rather than by the required accuracy of the modulated output signal frequency of the repeater system.

12. (Previously presented) The repeater system of claim 1, wherein the controller is arranged for implementing the following equation for obtaining the second control value:

$$CV_2 = f_{OUT} \cdot CV_1 / f_{IN}$$

wherein  $f_{OUT}$  represents the modulated output signal frequency,

$f_{IN}$  represents the modulate input signal, and

$CV_1$  represents the first control value.

13. (Previously presented) The repeater system of claim 2, wherein the controller is arranged for implementing the following equation for obtaining the second control value:

$$CV_2 = f_{OUT} / f_{IN} \cdot (a + CV_1) ] - d$$

wherein  $f_{OUT}$  represents the modulated output signal frequency,  $f_{IN}$  represents the modulated input signal,  $CV_1$  represents the first control value,  $a$  represents a fixed ratio between the tuner oscillator frequency and the repeater system clock frequency, and  $d$  represents a fixed ratio between the modulated output signal frequency and a repeater system clock frequency.

14. (Previously presented) The repeater system of claim 7, wherein the table length of both numerically controlled oscillators is 24 bits and wherein the multiplier is a 10-bit multiplier.

15. (Previously presented) The repeater system of claim 1, which is adapted for receiving a satellite signal as the modulated input signal, and for transmitting the modulated output signal into a terrestrial single frequency network.

16. (Previously presented) Method of receiving a modulated input signal and transmitting a modulated output signal, comprising the following steps:

providing a repeater system clock;

demodulating the modulated input signal or a signal which is derived from the modulated input signal, to obtain a demodulated signal wherein the step of demodulating comprises the following substeps:

mixing the modulated signal or the signal derived from the modulated signal using an oscillator signal output by a first controllable oscillator, which output signal is derived from the repeater system clock to obtain a mixed signal;

on the basis of the frequency of the mixed signal as an actual value and a desired value for the frequency of the mixed signal, determining the first control value applied to the first controllable oscillator such that the frequency of the mixed signal approaches the desired value;

modulating the demodulated signal to obtain the modulated output signal or a signal from which the modulated output signal is derived, wherein the step of demodulating comprises the following substeps:

mixing the demodulated signal or a signal which is derived from the demodulated signal using an oscillator signal output by a second controllable oscillator which output signal is derived from the repeater system clock;

determining a second control value, on the basis of the first control value and a predetermined value of the frequency of the modulated output signal, such that the frequency of the modulated output signal approaches a predetermined value; and

controlling the second controllable oscillator by applying the second control value to the second controllable oscillator.